

FCR010/FCR011 – fancoil controller (with UC010), RTC, 5 × DO, 2 × DI, 2 × RS485

- 50 words can be read at the same time (i.e. 100 bytes)
- whole range can be addressed bitwise
- the whole memory is mirrored as read-only from address 0x101 (e.g. 257 dec)

name	register	type	description	notes / defaults
module ID	1 LSB 1 MSB	R	module type identification	FCR010 - 0506hex FCR011 - 0507hex
firmware	2 LSB 2 MSB	R	firmware version	
status LSB	3 LSB	R, W RAM	module status lower byte bit 0 – write to EEPROM enabled bit 1 – SW reset enabled bit 4 – init EEPROM	Init EEPROM follows if the INIT switch was ON at power up, and if INIT switch was OFF at setting bit 4 to 1 (indicated by bit 2 in Status MSB)
status MSB	3 MSB	R, RAM	module status upper byte bit 0 0 normal mode 1 init mode bit 1 1 at the next EEPROM write attempt will all data be written to EEPROM 0 at the next EEPROM write attempt will received data be written to RAM only bit 2 – 1 – EEPROM initialized bit 3 – N/A bit 4 – 0 bit 5 – SW reset enabled bit 6 – 0 bit 7 – commissioning mode (1 – active)	SW reset enabled: see reg. 3 LSB bit 1, reg. 1002.
address	4 LSB	R,W EEPROM (0x01)	Modbus module address (for even distribution of load, fans and outputs are enabled after <i>address mod 10</i> secs)	!!! the change will be effective after restart only (however the register will be set immediately)

baud rate	4 MSB	R,W EEPROM	communication speed 10dec ... 1 200 bps 11dec ... 2 400 bps 12dec ... 4 800 bps 13dec ... 9 600 bps (default) 14dec ... 19 200 bps 15dec ... 38 400 bps 16dec ... 57 600 bps 17dec ... 115 200 bps	!!! the change will be effective after restart only (however the register will be set immediately)
serial port settings	5 LSB	R,W EEPROM	serial line parameter settings (default = no parity, 1 stop bit)	bit 0-1 ... parity (00 – no parity, 01 – even, 10 – odd) bit 2 ... stop bits (0 – one, 1 – two) !!! the change will be effective after restart only (however the register will be set immediately)
	5 MSB		reserved	
EEPROM writes	6 LSB 6 MSB	R, EEPROM	number of EEPROM writing cycles, does not overflow, cannot be reset by INIT nor by any other means	
relay	7 LSB	R, RAM	output relay status (DO1-DO5)	bit 0 ... relay 1, heating (and cooling when C/O) bit 1 ... relay 2, only cooling bit 2 ... relay 3, fan st. 1 bit 3 ... relay 4, fan st. 2 bit 4 ... relay 5, fan st. 3
inputs	7 MSB	R, RAM	digital input status (DI1 ... presence, DI2 ... window / alarm contact) and heat/cool demands; DI1 and DI2 are influenced by the inputs settings register settings. If the FCR010/FCR011 is a master (with multiple controllers with a single room unit), the slave DI states are included in this status. The master FCR010/FCR011 controller must have inputs enabled (input settings , bits 0 and 1) to include slave DIs.	bit 0 ... input DI1 bit 1 ... input DI2 bit 2 ... heating demand (PID output HEAT > 5%) bit 3 ... cooling demand (PID output COOL > 5%)

PID output HEAT	8 LSB	R, RAM	controller heating output (PID output, or value from manual control)	in %, range 0 .. 100%
PID output COOL	8 MSB	R, RAM	controller cooling output (PID output, or value from manual control, incl. change-over C/O mode)	in %, range 0 .. 100%
PID fan speed	9 LSB	R, RAM	fan speed state (PID output, or value from manual control)	0 ... off 1 ... Stage 1 2 ... Stage 2 3 ... Stage 3
UC comm. state	9 MSB	R, RAM	communication status of the room unit UC 010 (if comm fails for 60 secs, all controller outputs go to off (except for those controlled manually, see register manual control))	0 ... communication OK 1 ... timeout 2 ... MB exception 3 ... MB error
manual control	10 LSB	R, W RAM	manual output control; if a bit is set to 1, the output goes to state defined below (see manual fan speed, manual heat output, manual cool output); if set to 0, PID output values apply	bit 0 ... fan bit 1 ... heat output bit 2 ... cool output bit 3 to 4 ... reserved bit 5 ... C/O (1 = on)
manual fan speed	10 MSB	R, W RAM	manual fan speed setting (only if the corresponding bit in the manual control register is set)	0 ... off 1 ... Stage 1 2 ... Stage 2 3 ... Stage 3
manual heat output	11 LSB	R, W RAM	manual heat output setting (only if the corresponding bit in the manual control register is set)	in %, range 0 .. 100%
manual cool output	11 MSB	R, W RAM	manual cool output setting (only if the corresponding bit in the manual control register is set)	in %, range 0 .. 100%
set temp correction	12 LSB 12 MSB	R, W RAM	setpoint correction set by user; resets at each operation mode change 3.5°C reads 350 (limits are set in the min and max rel. temp correction registers)	recalculate: temperature = read value / 100 0 ... 0 199.99 ... 19999 -0.01 ... 0FFFFhex -199.99 ... 0B1E1hex

actual temp set point HEAT	13 LSB 13 MSB	R, RAM	actual heating setpoint incl. setpoint correction (reg. 12)	recalculate: temperature = read value / 100 0 ... 0 199.99 ... 19999 -0.01 ... 0FFFFhex -199.99 ... 0B1E1hex
actual temp set point COOL	14 LSB 14 MSB	R, RAM	actual cooling setpoint incl. setpoint correction (reg. 12)	recalculate: temperature = read value / 100 0 ... 0 199.99 ... 19999 -0.01 ... 0FFFFhex -199.99 ... 0B1E1hex
set day/ comfort heating temp	15 LSB 15 MSB	R,W EEPROM	day/comfort mode heating temperature setpoint set by user (default = 21°C, 0x0834)	recalculate: temperature = read value / 100 0 ... 0 199.99 ... 19999 -0.01 ... 0FFFFhex -199.99 ... 0B1E1hex
set night/ pre- comfort heating temp	16 LSB 16 MSB	R,W EEPROM	night/standby mode heating temperature setpoint set by user (default = 19°C, 0x076C)	recalculate: temperature = read value / 100 0 ... 0 199.99 ... 19999 -0.01 ... 0FFFFhex -199.99 ... 0B1E1hex
set depression/ economy heating temp	17 LSB 17 MSB	R,W EEPROM	off mode heating temperature setpoint set by user (default = 12°C, 0x04B0)	recalculate: temperature = read value / 100 0 ... 0 199.99 ... 19999 -0.01 ... 0FFFFhex -199.99 ... 0B1E1hex
set day/ comfort cooling temp	18 LSB 18 MSB	R,W EEPROM	day/comfort mode cooling temperature setpoint set by user (default = 24°C, 0x0960)	recalculate: temperature = read value / 100 0 ... 0 199.99 ... 19999 -0.01 ... 0FFFFhex -199.99 ... 0B1E1hex

set night/ pre-comfort cooling temp	19 LSB 19 MSB	R,W EEPROM	night/standby mode cooling temperature setpoint set by user (default = 26°C, 0x0A28)	recalculate: temperature = read value / 100 0 ... 0 199.99 ... 19999 -0.01 ... 0FFFFhex -199.99 ... 0B1E1hex
set depression/ economy cooling temp	20 LSB 20 MSB	R,W EEPROM	off mode cooling temperature setpoint set by user (default = 35°C, 0x0DAC)	recalculate: temperature = read value / 100 0 ... 0 199.99 ... 19999 -0.01 ... 0FFFFhex -199.99 ... 0B1E1hex
actual temp	21 LSB 21 MSB	R, RAM	actual temperature measured by the internal sensor incl. correction (see corr temp)	recalculate: temperature = read value / 100 0 ... 0 199.99 ... 19999 -0.01 ... 0FFFFhex -199.99 ... 0B1E1hex
actual outside temp	22 LSB 22 MSB	R, W RAM	actual outside temperature, may be written to RAM optionally for display	recalculate: temperature = read value / 100 0 ... 0 199.99 ... 19999 -0.01 ... 0FFFFhex -199.99 ... 0B1E1hex

set presence mode	23 LSB 23 MSB	R,W EEPROM	<p>presence status set by user (displayed symbols depend on the configuration register regulator settings, if set to hotel then comfort, standby, off; if set to residential then day, night, depression, auto, party)</p> <p>(default = comfort/day, 0x0001)</p>	<p>bit 0 ... comfort (occupied house) <i>or</i> day (sun + occupied house) bit 1 ... standby (empty house) <i>or</i> night (moon + occupied house) bit 2 ... off (off) <i>or</i> depression (empty house) bit 3 ... auto (clock) – <i>only when residential</i> bit 4 ... party (sun + drink + clock, after 2h goes to auto) – <i>only when residential</i> bit 5 to 14 ... reserved bit 15 ... write enable (if set to 1 value will be written into register, if in 0 attempt will be ignored)</p>
set fan mode	24 LSB 24 MSB	R,W EEPROM	<p>fan status set by user; if the fan is on Manual Off the cooling/heating valves are closed</p> <p>(default = auto, 0x0001)</p>	<p>bit 0 ... Auto (fan + A) bit 1 ... Off (fan + M) bit 2 ... Man 1 (fan + M + Stage1) bit 3 ... Man 2 (fan + M + Stage1 and 2) bit 4 ... Man 3 (fan + M + Stage1, 2 and 3) bit 5 to 14 ... reserved bit 15 ... write enable (if set to 1 value will be written into register, if in 0 attempt will be ignored)</p>

actual control mode	25 LSB	R, RAM	actual mode used for control, if on manual then the actual control mode is equal to set presence mode , if on auto then the actual control mode is according to time schedule (displayed symbols depend on the configuration register regulator settings , if set to hotel then comfort, standby, off; if set to residential then day, night, depression, auto, party)	bit 0 ... comfort/day bit 1 ... standby/night bit 2 ... off/depression
FC slaves communication state	25 MSB	R, RAM	status of communication with slave FCR010/FCR011 controllers (if multislave configuration)	0 ... OK 1 etc. ... first FCR010/FCR011 address which has comm fail
regulator settings	26 LSB	R,W EEPROM	controller configuration (defaults = hotel, relative, fan is on at heat and cool, 3 stage fan, valve protection on, 0x40)	bit 0 ... presence mode type (0 – hotel, 1 - residential) bit 1 ... temperature correction display (0 –relative, 1 – absolute) bit 2 ... 1 - stop fan when HEAT, 0 – fan can be on when HEAT) bit 3 ... 1 - stop fan when COOL, 0 – fan can be on when COOL) bits 4-5 ... fan type (00 – 3 stages, 01 – 2 stages, 10 – 1 stage) bit 6 ... valve exercising (1 – enabled) bit 7 ... heating valve polarity on DO1 (0 – NC, 1 – NO)

inputs	26 MSB	R,W EEPROM	inputs configuration DI1 ... presence DI2 ... window / alarm contact (default = inputs enabled, normally open, i.e. active when contact on, 0x0F)	bit 0 ... DI1 enabled for operation mode control bit 1 ... DI2 enabled for operation mode control bit 2 ... DI1 sense (0- NC- normally closed, 1-NO- normally open) bit 3 ... DI2 sense (0- NC - normally closed, 1- NO- normally open)
P band	27 LSB 27 MSB	R,W EEPROM	controller P-band (input deviation for output proportional part of 100 %) or hysteresis if On/Off (hysteresis for heating is under the setpoint, for cooling above the set-point) PI or OnOff setting see Reg. 29, bit 4	in 0.1 K (2 K, 0x0014)
I const	28 LSB 28 MSB	R,W EEPROM	controller I – constant; if out of bounds, a new recalculated value is set after restart	in seconds; if set to 0, integration part is disabled (60 min, 0x0E10)

regulator settings 2	29 LSB	R,W EEPROM	controller configuration (defaults = fan stage reset enable, slave off, DI2 as window contact, correction reset enable, PI control, 4-pipe, 0x89)	bit 0 ... fan stages reset to Auto when scheduler changes the presence mode bit 1 ... enable FC Slave function (controller will not communicate with UC010) – change only applies after restart bit 2 ... DI2 as alarm input, switches off all outputs bit 3 .. temp correction reset to 0 when presence mode changes bit 4 ... control mode (0 – PI, 1 – on/off) bit 5 ... cooling valve polarity on DO2 (0 – NC, 1 – NO) bit 6 ... reserved bit 7 ... fancoil type: 0: 2-pipe, 1: 4-pipe.
multi-slave number	29 MSB	R,W EEPROM	amount of slave FCR010/FCR011 (connected to the same bus as UC010, starting with Modbus address 10) – if this value is non-zero, the controller acts as a master (change is applied after device restart); from slave modules inputs are read and added to master inputs to control the controller mode (default = 0)	0 ... multi-slave function is off 1 or above ... number of FCR010/FCR011 slave modules
	30 LSB 30 MSB		reserved	
latch enable	31 LSB	R, W RAM	Latch enable function for individual inputs: By writing 1 into the register the particular bit in the latched value register goes to 0 and is kept until the required value is caught. After reset, the whole register is set to 0.	Resetting of the individual caught bits in the latched Value register : change the particular bit from log. 0 to log.1 (disable and enable the latch function for individual bits)

	31 MSB		reserved	
latched values	32 LSB	R RAM	<p>latched values</p> <p>0 – since latch enable there was no change on the bit</p> <p>1 - since latch enable the bit value has changed its state</p>	<p>bit 0 is input 1</p> <p>bit 1 is input 2</p> <p>to reset the bits, disable and enable latch - see latch enable</p>
	32 MSB		reserved	
min rel. temp correction	33 LSB 33 MSB	R,W EEPROM	<p>minimum relative user temperature correction, a positive value is saved and is taken as negative limit</p>	<p>recalculate:</p> <p>minimum correction = -(read value/100);</p> <p>-10.00 ... 1000</p> <p>(-3.5 °C, 0x015E)</p>
max rel. temp correction	34 LSB 34 MSB	R,W EEPROM	<p>maximum relative user temperature correction</p>	<p>recalculate:</p> <p>maximum correction = (read value/100);</p> <p>10.00 ... 1000</p> <p>(3.5 °C, 0x015E)</p>
min day, night, depression temp	35 LSB 35 MSB	R,W EEPROM	<p>minimum temperature which user can set as setpoint for day, night, and off modes</p> <p>-199.99 to 199.99</p> <p>(default = 10 °C, 0x03E8)</p>	<p>recalculate:</p> <p>temperature = read value / 100</p> <p>0 ... 0</p> <p>199.99 ... 19999</p> <p>-0.01 ... 0FFFFhex</p> <p>-199.99 ... 0B1E1hex</p>
max day, night, depression temp	36 LSB 36 MSB	R,W EEPROM	<p>maximum temperature which user can set as setpoint for day, night, and off modes</p> <p>-199.99 to 199.99</p> <p>(default = 40 °C, 0x0FA0)</p>	<p>recalculate:</p> <p>temperature = read value / 100</p> <p>0 ... 0</p> <p>199.99 ... 19999</p> <p>-0.01 ... 0FFFFhex</p> <p>-199.99 ... 0B1E1hex</p>
corr temp	37 LSB 37 MSB	R,W EEPROM	<p>correction: adds to the actual temperature measured by the internal sensor</p> <p>-20.00 to 20.00</p> <p>(default = -1,5 K, 0xFF6A)</p>	<p>recalculate:</p> <p>temperature = read value / 100</p> <p>0 ... 0</p> <p>199.99 ... 19999</p> <p>-0.01 ... 0FFFFhex</p> <p>-199.99 ... 0B1E1hex</p>

latch state	38 LSB	R,W EEPROM	state which will be caught: 0 – it will be caught log. 0 1 – it will be caught log. 1 (caught log. 0, 0x00)	bit 0 is input 1 bit 1 is input 2
presence off delay	38 MSB	R,W EEPROM	time delay after presence signal off (by DI1), default = 0	[0 ... 255 min]
step temp	39 LSB	R,W EEPROM	step for user temperature setpoints setting (default = 0.5 °C, 0x32)	step = read value / 100 1 ... 0.01 50 ... 0.5 100 ... 1 etc.
step minutes	39 MSB	R,W EEPROM	time step for time schedule setting (default = 5 mins, 0x05)	in minutes
	40 LSB 40 MSB		reserved	
show mode	41 LSB	R,W RAM	data that roll on the LCD display (default = 1) If only one of the bits is active there is only one value displayed. Otherwise they change periodically after show time .	bit 0 ... temperature bit 1 ... outside temp. bit 2 ... current time bit 3 ... temperature correction (default = temperature, 0x0001)
fan start delay	41 MSB	R,W RAM	delay after valve open signal to fan start (if 0 – function off) For heating only. Prevents cold air boost at morning starts.	[in tens of second] 2 ... 20 seconds
show time	42 LSB	R,W EEPROM	time (in 100 ms) to display each value in show mode (default = 3 s, 0x1E)	see show mode
edit return time	42 MSB	R,W EEPROM	time (in s) of user inactivity to return from edit mode to show mode (default = 30 s, 0x1E)	
quick edit mode number	43 LSB	R,W EEPROM	number of mode which is editable through quick edit menu (short push of the knob) (default = fan mode, 0x02)	0 ... push function inactive 1 ... presence mode 2 ... fan mode

change-over period	43 MSB	R,W EEPROM	time delay when switching between heating and cooling modes (default = 30 min, 0x1E)	in minutes, 1...255
long push time	44 LSB	R,W EEPROM	time (in 100 ms) evaluated as long push (go to time schedule menu / leave menu) (default = 1.5 s)	for editing of the time schedule and presence or fan mode
super long push time	44 MSB	R,W EEPROM	time (in 100 ms) evaluated as superlong push (go to settings menu) (default = 5 s, 0x32)	for actual time and basic setpoints settings
allowed operation modes	45 LSB 45 MSB	R,W EEPROM	settings that user is able to perform 0 ... disabled 1 ... enabled (default = temp corr, fan mode 0x0201)	bit 0 ... temp corr. bit 1 ... heating day temp bit 2 ... heating night temp bit 3 ... heating depression temp bit 4 ... cooling day temp bit 5 ... cooling night temp bit 6 ... cooling depression temp bit 7 ... RTC time bit 8 ... presence mode bit 9 ... fan mode bit 10 ... time programme
presence mode edit mask	46 LSB 46 MSB	R,W EEPROM	states in presence mode that user is able to switch between (default = all, 0x001F)	bit 0 ... day (sun + occupied house) bit 1 ... night (moon + occupied house) bit 2 ... depression (empty house) bit 3 ... auto (clock) bit 4 ... party (sun + drink + clock, after 2h goes to auto)

fan mode edit mask	47 LSB 47 MSB	R,W EEPROM	fan states that user is able to switch between (default = all, 0x001F)	bit 0 ... Auto (fan + A) bit 1 ... Off (fan + M) bit 2 ... Man 1 (fan + M + Stage1) bit 3 ... Man 2 (fan + M + Stage1 and 2) bit 4 ... Man 3 (fan + M + Stage1, 2 and 3)
display symbols	48 LSB 48 MSB	R,W RAM	displayed symbols	bit 0 ... spanner bit 1 ... boiler bit 2 ... alarm bell bit 3 to 14 ... reserved bit 15 ... write enable (if set to 1 the value will be written into register, if in 0 attempt will be ignored)
RTC	49 LSB 49 MSB 50 LSB 50 MSB 51 LSB 51 MSB 52 LSB 52 MSB	R,W EEPROM	Real time clock (not subject to INIT command)	see table below; to write to those registers, write to EEPROM must be enabled in the status LSB register
	53 LSB 53 MSB		reserved	
program Monday Event 1 time	54 LSB 54 MSB	R,W EEPROM	time schedule, Monday, time of event No. 1, in mins since 0:00 (midnight) (default = 06:00 h, 0x0168)	e.g. 121 ... 2h 1min
program Monday Event 1 value	55 LSB 55 MSB	R,W EEPROM	time schedule, Monday, event No. 1, value (default = 0)	0 ... day/comfort 1 ... night/standby 2 ... off/depression bit 15 = 1 ... event is disabled
program Monday Event 2 time	56 LSB 56 MSB	R,W EEPROM	time schedule, Monday, time of event No. 2, in mins since 0:00 (midnight) (default = 08:00 h)	e.g. 121 ... 2h 1min
program Monday Event 2 value	57 LSB 57 MSB	R,W EEPROM	time schedule, Monday, event No. 2, value (default = 1)	0 ... day/comfort 1 ... night/standby 2 ... off/depression bit 15 = 1 ... event is disabled

program Monday Event 3 time	58 LSB 58 MSB	R,W EEPROM	time schedule, Monday, time of event No. 3, in mins since 0:00 (midnight) (default = 14:00 h)	e.g. 121 ... 2h 1min
program Monday Event 3 value	59 LSB 59 MSB	R,W EEPROM	time schedule, Monday, event No. 3, value (default = 0)	0 ... day/comfort 1 ... night/standby 2 ... off/depression bit 15 = 1 ... event is disabled
program Monday Event 4 time	60 LSB 60 MSB	R,W EEPROM	time schedule, Monday, time of event No. 4, in mins since 0:00 (midnight) (default = 22:00 h)	e.g. 121 ... 2h 1min
program Monday Event 4 value	61 LSB 61 MSB	R,W EEPROM	time schedule, Monday, event No. 4, value (default = 1)	0 ... day/comfort 1 ... night/standby 2 ... off/depression bit 15 = 1 ... event is disabled
program Monday Event 5 time	62 LSB 62 MSB	R,W EEPROM	time schedule, Monday, time of event No. 5, in mins since 0:00 (midnight) (default = 06:00 h, 0x0168)	e.g. 121 ... 2h 1min
program Monday Event 5 value	63 LSB 63 MSB	R,W EEPROM	time schedule, Monday, event No. 5, value (default = disabled, 0x8000)	0 ... day/comfort 1 ... night/standby 2 ... off/depression bit 15 = 1 ... event is disabled
program Monday Event 6 time	64 LSB 64 MSB	R,W EEPROM	time schedule, Monday, time of event No. 6, in mins since 0:00 (midnight) (default = 06:00 h)	e.g. 121 ... 2h 1min
program Monday Event 6 value	65 LSB 65 MSB	R,W EEPROM	time schedule, Monday, event No. 6, value (default = disabled)	0 ... day/comfort 1 ... night/standby 2 ... off/depression bit 15 = 1 ... event is disabled
program Tuesday Event 1 time	66 LSB 66 MSB	R,W EEPROM	time schedule, Tuesday, time of event No. 1, in mins since 0:00 (midnight) (default = 06:00 h)	e.g. 121 ... 2h 1min
...

program Sunday Event 6 value	137 LSB 137 MSB	R,W EEPROM	time schedule, Sunday, event No. 6, value (default = disabled)	0 ... day/comfort 1 ... night/standby 2 ... off/depression bit 15 = 1 ... event is disabled
Regulator settings 4	158	R, W EEPROM (AO1/2 type NC 0x0)	setting the polarity of the analog outputs	bit 0 ... polarity of AO1 output (0 - NC 1 - NO) bit 1 ... polarity of AO2 output (0 - NC 1 - NO)
uptime	1000 LSB 1000 MSB 1001 LSB 1001 MSB	R	uptime (s)	number of seconds after last power on / reset
reset	1002 LSB 1002 MSB	W, RAM	reset device	The device resets if reset is enabled (see reg. 3), and a non- zero value is written to this register.

Real time table

Addr.	bit7	bit6	bit5	bit4	bit3	bit2	bit1	bit0	Function	Range
49 LSB		10xsecs			seconds				secs	00-59
49 MSB	0	10xmins			minutes				mins	00-59
50 LSB	0	10xhours		10xhours	hours				hours	00-23
50 MSB	0	0	0	0	0	day		day	01-07	
51 LSB	0	0	10xdate		date				date	01-31
51 MSB	0	0	0	10xmonth	month				month	01-12
52 LSB	10xyear			year				year	00-99	
52 MSB	0	0	0	0	0	0	0	0	not used	00

Commissioning mode: when powered on with pushbutton pushed, after several seconds the commissioning mode is activated with manual control over outputs and indication of inputs. After 10 mins after last push or after reboot the controller goes to normal (control) mode. In the commissioning mode, all Modbus data are read-only.

Revision:

08. 03. 2022 ver. 111 – add 26 LSB bit 7, 29 LSB bit 5 and reg. 158