

- max 8 words may be read out as a whole (i.e. 16 bytes)
- first 128 bits can be addressed bitwise (i.e. the whole map)
- OC - open collector

Name	Address	Type	Description	Note
module ID LSB	1 LSB	R	module identification lower byte	module ID is 0030hex
module ID MSB	1 MSB	R	module identification upper byte	
firmware LSB	2 LSB	R	firmware version lower byte	01
firmware MSB	2 MSB	R	firmware version upper byte	00
status LSB	3 LSB	R, W RAM	module status lower byte bit 0 - EEPROM write enable	
status MSB	3 MSB	R	module status upper byte bit 0 - 0 normal mode - 1 init mode bit 1 - 1 at the next EEPROM write attempt all data will be saved to EEPROM - 0 at the next write attempt received data will be written to RAM only bit 2 reserved bit 3 reserved bit 4 - 0 bit 5 - 1 bit 6 - 0 bit 7 - 1	
address	4 LSB	R,W EEPROM	module address (0x01)	The changes will become active only after module restart (the register is written immediately, but the new address is effective after restart)
baud rate (communication speed)	4 MSB	R,W EEPROM	no parity 10dec ... 1200 bps 11dec ... 2400 bps 12dec ... 4800 bps 13dec ... 9600 bps 14dec ... 19200 bps 15dec ... 38400 bps 16dec ... 57600 bps 17dec ... 115200 bps	The changes will become active only after module restart (the register is written immediately, the new baud rate is effective after restart)

M300 Modbus table
8 DO open collector

Release 10.2.2009 ver. 100



relay	5 LSB	R,W RAM	OC outputs on / off	bit 0 is OC 1 ... bit 7 is OC 8
reserved	5 MSB	R,W RAM		
relay com	6 LSB	R,W EEPROM	0 – when no communication, OCs stay in last state 1 – when no communication, OCs are set to relay state values	bit 0 is OC 1 ... bit 7 is OC 8
relay state	6 MSB	R,W EEPROM	OCs go on or off (according to corresponding bits) if there was no communication with module for a given time and in relay com the corresponding OC bit is set to 1	bit 0 is OC 1 ... bit 7 is OC 8
relay time	7 LSB	R,W EEPROM	time in [s] of no communication which is considered as communication failure	if set to 0, the function is disabled
relay start enable	7 MSB	R,W EEPROM	startup OC behaviour 0 – OCs are not commanded 1 – the corresponding OC is set to its relay start value after module startup	bit 0 is OC 1 ... bit 7 is OC 8
relay start	8 LSB	R,W EEPROM	OC status between power-up and first bus command	if set to 0, the function is disabled
reserved	8 MSB	R,W EEPROM		