

- max 10 words may be read out as a whole (i.e. 20 bytes)
- first 160 bits can be addressed bitwise (i.e. the whole map)
- OC = open collector

Name	Address	Type	Description	Note
module ID LSB	1 LSB	R	module identification lower byte	module ID is 0032 <sub>hex</sub>
module ID MSB	1 MSB	R	module identification upper byte	
firmware LSB	2 LSB	R	firmware version lower byte	01
firmware MSB	2 MSB	R	firmware version upper byte	00
status LSB	3 LSB	R, W RAM	module status lower byte <b>bit 0</b> – EEPROM write enable	
status MSB	3 MSB	R	module status upper byte <b>bit 0</b> - 0 normal mode - 1 init mode <b>bit 1</b> - 1 at the next EEPROM write attempt all data will be saved to EEPROM - 0 at the next write attempt all data will be written to RAM only <b>bit 2</b> - reserved <b>bit 3</b> - reserved <b>bit 4</b> - 0 <b>bit 5</b> - 1 <b>bit 6</b> - 0 <b>bit 7</b> - 1	
<b>address</b>	4 LSB	R,W EEPROM	module address (0x01)	The changes will become active only after module restart (the register is written immediately, but the new address is effective after restart)
baud rate (communication speed)	4 MSB	R,W EEPROM	no parity 10 <sub>dec</sub> ... 1200 bps 11 <sub>dec</sub> ... 2400 bps 12 <sub>dec</sub> ... 4800 bps 13 <sub>dec</sub> ... 9600 bps 14 <sub>dec</sub> ... 19200 bps 15 <sub>dec</sub> ... 38400 bps 16 <sub>dec</sub> ... 57600 bps 17 <sub>dec</sub> ... 115200 bps	The changes will become active only after module restart (the register is written immediately, the new baud rate is effective after restart)

**M320 Modbus table**  
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relay low	5 LSB	R,W RAM	OC outputs on / off	bit 0 is OC1 bit 1 is OC2 bit 2 is OC3 bit 3 is OC4 bit 4 is OC5 bit 5 is OC6 bit 6 is OC7 bit 7 is OC8
relay high	5 MSB	R,W RAM	OC outputs on / off	bit 0 is OC9 bit 1 is OC10 bit 2 is OC11 bit 3 is OC12 bit 4 is OC13 bit 5 is OC14 bit 6 is OC15 bit 7 is OC16
relay com low	6 LSB	R,W EEPROM	<b>0</b> – when no communication, relays stay in last state <b>1</b> – when no communication, relays are set to <b>relay state</b> values	bit 0 is OC1 ... bit 7 is OC8
relay com high	6 MSB	R,W EEPROM	<b>0</b> – when no communication, relays stay in last state <b>1</b> – when no communication, relays are set to <b>relay state</b> values	bit 0 is OC9 ... bit 7 is OC16
relay state low	7 LSB	R,W EEPROM	OCs go on or off (according to corresponding bits) if there was no communication with module for a given time and in <b>relay com</b> the corresponding relay bit is set to 1	bit 0 is OC1 ... bit 7 is OC8
relay state high	7 MSB	R,W EEPROM	OCs go on or off (according to corresponding bits) if there was no communication with module for a given time and in <b>relay com</b> the corresponding relay bit is set to 1	bit 0 is OC9 ... bit 7 is OC16
relay time	8 LSB	R,W EEPROM	time in [s] of no communication which is considered as communication failure	if set to 0, the function is disabled
reserved	8 MSB	R, W RAM		
relay start enable low	9 LSB	R,W EEPROM	startup OC behaviour <b>0</b> – relays are not commanded <b>1</b> – the corresponding relay is set to its <b>relay start</b> value after module startup	bit 0 is OC1 ... bit 7 is OC8

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relay start enable high	9 MSB	R,W EEPROM	startup OC behaviour <b>0</b> – relays are not commanded <b>1</b> – the corresponding relay is set to its <b>relay start</b> value after module startup	bit 0 is OC9 ... bit 7 is OC16
relay start low	10 LSB	R,W EEPROM	relay status between power-up and first bus command	bit 0 is OC1 ... bit 7 is OC8
relay start high	10 MSB	R,W EEPROM	relay status between power-up and first bus command	bit 0 is OC9 ... bit 7 is OC16