

- **max 8 words may be read out as a whole (i.e. 16 bytes)**
- **first 240 bits can be addressed bitwise (i.e. the whole map)**

Name	Address	Type	Description	Note
module ID LSB	1 LSB	R	module identification lower byte	module ID is 0054 <sub>hex</sub>
module ID MSB	1 MSB	R	module identification upper byte	
firmware LSB	2 LSB	R	firmware version lower byte	107 <sub>hex</sub>
firmware MSB	2 MSB	R	firmware version upper byte	
status LSB	3 LSB	R, W RAM	module status lower byte <b>bit 0</b> - EEPROM write enable <b>bit 4</b> - EEPROM init <b>bit 5</b> - calibration offset <b>bit 6</b> - calibration span <b>bit 7</b> - calibration enable	<b>EEPROM init</b> is enabled when the INIT switch was ON at power-up, and switched OFF before bit 4 was set to 1 (indicated by bit 2 in status MSB) <b>calibration</b> is enabled when the INIT switch was ON at power-up, and switched OFF before bit 7 was set to 1 (indicated by bit 3 in status MSB) <b>calibration offset</b> change bit 7 from 1 to 0 and set bit 5 to 1 <b>calibration span</b> change bit 7 from 1 to 0 and set bit 6 to 1
status MSB	3 MSB	R	module status upper byte <b>bit 0</b> - 0 normal mode - 1 init mode <b>bit 1</b> - 1 at the next EEPROM write attempt all data will be saved to EEPROM - 0 at the next write attempt received data will be written to RAM only <b>bit 2</b> - 1 - EEPROM initialised <b>bit 3</b> - 1 - calibration enabled <b>bit 4</b> - 0 <b>bit 5</b> - 1 <b>bit 6</b> - 0 <b>bit 7</b> - 1	
<b>address</b>	4 LSB	R,W EEPROM	module address (0x01)	The changes will become active only after module

				restart (the register is written immediately, but the new address is effective after restart)
baud rate (communication speed)	4 MSB	R,W EEPROM	no parity 10dec ... 1200 bps 11dec ... 2400 bps 12dec ... 4800 bps 13dec ... 9600 bps 14dec ... 19200 bps 15dec ... 38 400bps 16dec ... 57 600bps 17dec ... 115 200bps	The changes will become active only after module restart (the register is written immediately, the new baud rate is effective after restart)
input range for inputs 1, 2	5 LSB	R,W EEPROM	0Ahex ... voltage -10 to 10 V 0Bhex ... voltage -5 to 5 V 0Chex ... voltage -1 to 1 V 0Dhex ... voltage -0.5 to 0.5 V 0Ehex ... voltage -0.15 to 0.15 V 0Fhex ... current -20 to 20 mA (external resistance 125 ohm necessary, set via DIP switch)	bit 0 to bit 3: input 1 bit 4 to bit 7: input 2
input range for inputs 3, 4	5 MSB	R,W EEPROM		bit 0 to bit 3: input 3 bit 4 to bit 7: input 4
reserved	6 LSB	R,W EEPROM	not used	
reserved	6 MSB	R,W EEPROM	not used	
input 1 value	7 LSB, 7 MSB	R	0...65535 according to range (0000hex - FFFFhex)	0... low limit, e.g. 0 V 65535...high limit, e.g. 10 V
input 2 value	8 LSB, 8 MSB	R		
input 3 value	9 LSB, 9 MSB	R		
input 4 value	10 LSB, 10 MSB	R		
reserved	11 LSB, 11 MSB	R	not used	
reserved	12 LSB, 12 MSB	R	not used	
reserved	13 LSB, 13 MSB	R	not used	
reserved	14 LSB, 14 MSB	R	not used	
inputs to measure	15 LSB	R,W EEPROM	bit map of inputs which will be measured, 0... not active 1... active	bit 0: AI1 bit 1: AI2 bit 2: AI3 bit 3: AI4

				default: 0xF, all 4 inputs active
reserved	15 MSB	R		
uptime 1	16 LSB	R	time in seconds since module power-up or reset	LSB
uptime 2	16 MSB	R		
uptime 3	17 LSB	R		
uptime 4	17 MSB	R		MSB
number of EE write cycles - values 1	18 LSB	R	number of EEPROM writing cycles (address, baud rate, range...), just for information	counter 0...FFFE; no overflow. When FFFE is reached, the counter stops.
number of EE write cycles - values 2	18 MSB	R		
number of EE write cycles - calibration 1	19 LSB	R	number of EEPROM writing cycles - calibration	counter 0...FFFE; no overflow. When FFFE is reached, the counter stops.
number of EE write cycles - calibration 2	19 MSB	R		