

- max 8 words may be read out as a whole (i.e. 16 bytes)
- first 240 bits can be addressed bitwise

Name	Address	Type	Description	Note
module ID LSB	1 LSB	R	module identification lower byte	module ID is 0058hex
module ID MSB	1 MSB	R	module identification upper byte	
firmware LSB	2 LSB	R	firmware version lower byte	107hex
firmware MSB	2 MSB	R	firmware version upper byte	
status LSB	3 LSB	R, W RAM	module status lower byte bit 0 – EEPROM write enable bit 4 – EEPROM init bit 5 – calibration offset bit 6 – calibration span bit 7 – calibration enable	EEPROM init is enabled when the INIT switch was ON at power-up, and switched OFF before bit 4 was set to 1 (indicated by bit 2 in status MSB) calibration is enabled when the INIT switch was ON at power-up, and switched OFF before bit 7 was set to 1 (indicated by bit 3 in status MSB) calibration offset change bit 7 from 1 to 0 and set bit 5 to 1 calibration span change bit 7 from 1 to 0 and set bit 6 to 1
status MSB	3 MSB	R	module status upper byte bit 0 - 0 normal mode - 1 init mode bit 1 - 1 at the next EEPROM write attempt all data will be saved to EEPROM - 0 at the next write attempt received data will be written to RAM only bit 2 - 1 – EEPROM initialised bit 3 - 1 – calibration enabled bit 4 - 0 bit 5 - 1 bit 6 - 0 bit 7 - 1	
address	4 LSB	R,W EEPROM	module address (0x01)	The changes will become active only after module restart (the register is

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				written immediately, but the new address is effective after restart)
baud rate (communication speed)	4 MSB	R,W EEPROM	no parity 10 _{dec} ... 1200 bps 11 _{dec} ... 2400 bps 12 _{dec} ... 4800 bps 13 _{dec} ... 9600 bps 14 _{dec} ... 19200 bps	The changes will become active only after module restart (the register is written immediately, the new baud rate is effective after restart)
input range for inputs 1, 2	5 LSB	R,W EEPROM	0A _{hex} ... voltage -4 to 4 V for sensor range -50 to 50 A	bit 0 to bit 3: input 1 bit 4 to bit 7: input 2
input range for inputs 3, 4	5 MSB	R,W EEPROM		bit 0 to bit 3: input 3 bit 4 to bit 7: input 4
input range for inputs 5, 6	6 LSB	R,W EEPROM		bit 0 to bit 3: input 5 bit 4 to bit 7: input 6
input range for inputs 7, 8	6 MSB	R,W EEPROM		bit 0 to bit 3: input 7 bit 4 to bit 7: input 8
input 1 value	7 LSB, 7 MSB	R	0...65535 according to range (0000 _{hex} - FFFF _{hex})	0... low limit 65535...high limit
input 2 value	8 LSB, 8 MSB	R		
input 3 value	9 LSB, 9 MSB	R		
input 4 value	10 LSB, 10 MSB	R		
input 5 value	11 LSB, 11 MSB	R		
input 6 value	12 LSB, 12 MSB	R		
input 7 value	13 LSB, 13 MSB	R		
input 8 value	14 LSB, 14 MSB	R		
inputs to measure	15 LSB	R,W EEPROM	bit map of inputs which will be measured, 0... not active 1... active	bit 0: AI1 ... bit 7: AI8 default: 0xFF, all 8 inputs active
reserved	15 MSB	R		
uptime 1	16 LSB	R	time in seconds since module power-up or reset	LSB
uptime 2	16 MSB	R		
uptime 3	17 LSB	R		
uptime 4	17 MSB	R		MSB

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number of EE write cycles - values 1	18 LSB	R	number of EEPROM writing cycles (address, baud rate, range...), just for information	counter 0...FFFE; no overflow. When FFFE is reached, the counter stops.
number of EE write cycles - values 2	18 MSB	R		
number of EE write cycles - calibration 1	19 LSB	R	number of EEPROM writing cycles - calibration	counter 0...FFFE; no overflow. When FFFE is reached, the counter stops.
number of EE write cycles - calibration 2	19 MSB	R		