

M610 Modbus table

8 AO 0...10 V

Release 10.2.2009 ver. 100

domat
control system

- max 14 words may be read out as a whole (i.e. 28 bytes)
- first 224 bits can be addressed bitwise (i.e. whole map)

Name	Address	Type	Description	Note
module ID LSB	1 LSB	R	module identification lower byte	module ID is 0061hex
module ID MSB	1 MSB	R	module identification upper byte	
firmware LSB	2 LSB	R	firmware version lower byte	01
firmware MSB	2 MSB	R	firmware version upper byte	00
status LSB	3 LSB	R, W RAM	module status lower byte bit 0 – EEPROM write enable bit 4 – EEPROM init	EEPROM init is enabled when the INIT switch was ON at power-up, and switched OFF before bit 4 was set to 1 (indicated by bit 2 in status MSB)
status MSB	3 MSB	R	module status upper byte bit 0 - 0 normal mode - 1 init mode bit 1 - 1 at the next EEPROM write attempt all data will be saved to EEPROM - 0 at the next write attempt received data will be written to RAM only bit 2 - 1 – EEPROM initialised bit 3 reserved bit 4 - 0 bit 5 - 1 bit 6 - 0 bit 7 - 1	
address	4 LSB	R,W EEPROM	module address (0x01)	The changes will become active only after module restart (the register is written immediately, but the new address is effective after restart)
baud rate (communication speed)	4 MSB	R,W EEPROM	no parity 10 _{dec} ... 1200 bps 11 _{dec} ... 2400 bps 12 _{dec} ... 4800 bps 13 _{dec} ... 9600 bps 14 _{dec} ... 19200 bps 15 _{dec} ... 38 400bps 16 _{dec} ... 57 600bps 17 _{dec} ... 115 200bps	The changes will become active only after module restart (the register is written immediately, the new baud rate is effective after restart)

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output range for outputs 1, 2	5 LSB	R,W EEPROM	2 ... voltage 0 to 10 V	bit 0 to bit 3: output 1 bit 4 to bit 7: output 2
output range for outputs 3, 4	5 MSB	R,W EEPROM		bit 0 to bit 3: output 3 bit 4 to bit 7: output 4
output range for outputs 5, 6	6 LSB	R,W EEPROM		bit 0 to bit 3: output 5 bit 4 to bit 7: output 6
output range for outputs 7, 8	6 MSB	R,W EEPROM		bit 0 to bit 3: output 7 bit 4 to bit 7: output 8
output 1 value	7 LSB, 7 MSB	R,W EEPROM	0...4095 according to range (0000 _{hex} - 0FFF _{hex})	0... low limit, e.g. 0 V 4095...high limit, e.g. 10 V
output 2 value	8 LSB, 8 MSB	R,W EEPROM		
output 3 value	9 LSB, 9 MSB	R,W EEPROM		
output 4 value	10 LSB, 10 MSB	R,W EEPROM		
output 5 value	11 LSB, 11 MSB	R,W EEPROM		
output 6 value	12 LSB, 12 MSB	R,W EEPROM		
output 7 value	13 LSB, 13 MSB	R,W EEPROM		
output 8 value	14 LSB, 14 MSB	R,W EEPROM		